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09/715,335	11/17/2000	Kevin Lefebvre	10005281-1	6005

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EXAMINER

QUILLEN, ALLEN E

ART UNIT

PAPER NUMBER

2676

DATE MAILED: 08/06/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

09/715,335

Applicant(s)

LEFEBVRE ET AL.

Examiner

Allen E. Quillen

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 19 May 2003.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-51 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-51 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.  
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

## Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☐ All b) ☐ Some \* c) ☐ None of:  
1. ☐ Certified copies of the priority documents have been received.  
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).  
\* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).  
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) \_\_\_\_\_ 6) ☐ Other: \_\_\_\_\_

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## DETAILED ACTION

### *Response to Amendment*

1. Claims 1, 9, 12, 15, 18, and 21-30 are amended; claims 39-51 are added. All claims pending. Applicant's arguments with respect to claims 1-51 have been considered but are moot in view of the new ground(s) of rejection.

### *Double Patenting*

2. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

3. Claim 1, 15, 18, 21, 43, 48 are provisionally rejected under the judicially created doctrine of provisional obviousness-type double patenting as being unpatentable over claims 1, 7, 11 and 12 of copending Application No. 09/715253, Claim 2 of copending Application No. 09/715892, and Claim 1 of copending Application No. 09/715,746. This is a provisional double patenting rejection since the conflicting claims have not in fact been patented. The conflicting claims are not identical; they are patentably distinct from each other because current application 09/715,746 additionally recites "plurality of display devices". Applications No. 09/715253 and Application

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No. 09/715335 do not recite "plurality of display devices". At the time of the invention, it would have been obvious to one skilled in the art of computer graphics processing to use a plurality of displays to achieve a larger-sized display. Co pending Application 09/715,892 cites digital video data stream and pixel processing. At the time of the invention, it would have been obvious to one skilled in the art of computer graphics processing to use pixel processing with graphics processors in combination with video data.

*Claim Rejections - 35 USC § 103*

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

The factual inquiries set forth in *Graham v. John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

1. Determining the scope and contents of the prior art.
2. Ascertaining the differences between the prior art and the claims at issue.
3. Resolving the level of ordinary skill in the pertinent art.
4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out

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the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

5. Claims 1-51 are rejected under 35 U.S.C. 103(a) as being unpatentable over MacInnis, et al, U.S. Patent 6,573,905 in view of Jenkins, U.S. Patent 6,111,582.

6. Regarding claim 1, representative of claims 15, 18, 21, 43-46, 48-51, MacInnis discloses a graphical display system (Figures 1-7, 48, 60-64, 69-70, 73-74; Column 6, lines 21, 29-33), comprising: a first graphics pipeline (Figure 69, Column 112, lines 24-33) configured to receive graphical data transmitted from a graphics application ([Claim 44] *Graphics data for display is.... Direct Draw...Microsoft...read from memory*, Column 5, lines 25-28) and to render said graphical data received by said first graphics pipeline (Figure 69, Column 112, lines 24-33); a second graphics pipeline configured to receive graphical data transmitted from said graphics application and to render said graphical data received by said second graphics pipeline (Figure 69, Column 112, lines 24-33); a display device configured to display an image (Figure 62, element 2518); and a compositor (Figure 4, element 108, Column 10, lines 41-43) configured to receive said graphical data rendered by said first graphics pipeline and said graphical data rendered by said second graphics pipeline, said compositor further configured to interface said graphical data received by said compositor with said display device, wherein said image is based on said graphical data received by said compositor (Figures 62, 69; Column 52, lines 18-19; Column 52, lines 19-23; Column 46, lines 1-7);

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[Claim 18] the compositor (graphics and video, Figure 4, element 108, Column 7, lines 66 through Column 8, line 13) configured to receive said first and second graphical data portions from said first and second pipelines and to interface said first and second graphical data portions with said display device (Figures 62, 69, HDTV, Column 112, lines 24-32) wherein a first portion of said image is based on said first graphical data portion and wherein said first and second graphics pipelines render in parallel (Column 112, lines 24-32).

[Claim 21] receiving a graphical command, said graphical command including graphical data (*start code...processes the incoming video elementary stream in accordance with the type of start code...start code...stores incoming video elementary stream in the external memory*, Column 77, lines 17-22; 41-52; *header and graphics content*, Column 109, lines 50-61);

[Claims 43-46, 48, 51] logic configured to receive graphical data defining a graphical object (*2D, windows descriptors, x, y*, Column 15, lines 51-53; Column 16, lines 36-65 Column 59, line 10; Column 50, lines 19-59), the logic configured to control said first graphics pipeline such that said first graphics pipeline renders, based on said graphical data, a first portion of said graphical object without rendering a second portion of said graphical object, said logic further configured to control said second graphics pipeline such that said graphics pipeline renders, based on said graphical data, said second portion of said graphical data without rendering said first portion (*blending, layers, at some point, there can be a single image representing the lower layers*, (Column 47, line 60-65), Column 45, line 55 through Column 51, line 33).

MacInnis does not disclose and a second portion of said image is based on said second graphical data portion, rendering said first and second graphical portions of the image. Jenkins teaches multiple pipelines (Column 60, lines 29-63); wherein said first and second images define

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at least a portion of a image; second rendering means for rendering a respective second portion of said graphical data, said second rendering means including a plurality of pipeline means for rendering said second graphical data portion in parallel and a compositing means for compositing said second rendered portion (Figures 1-4, Figure 13A, *sub-images, being handled by one of the sub-image processors*, Column 17, lines 52-55). The motivation for combining multiple pipeline processing with multiple sub-images with respective rendering means is for efficient use of available connection bandwidth and allow rapid synchronization with broadcast event stream, load balancing, computational efficiency, level of detail and resolution suited to human needs (Column 114, lines 51-54, 63-63; Column 117, lines 1-6, 24, 38-40, 48-50, 63-67, Column 118, lines 1-9, 17-28, 36-38, 55-65). Jenkins is evidence that at the time of the invention it would have been obvious to one skilled in the art of computer graphics processing to combine the benefits of parallel pipeline processing, as MacInnis discloses, with multiple sub-images rendering, as Jenkins teaches, for performance and display efficiencies.

7. Regarding claim 2, MacInnis discloses the system of claim 1, wherein said first graphics pipeline and said second graphics pipeline simultaneously and in parallel process said graphical data rendered by said first and second graphics pipelines (see above, Column 112, lines 24-32).

8. Regarding claim 3, representative of claims 23, 25, 26, 30, 47, MacInnis discloses the system of claim 1, further comprising: an input device configured to receive an input from a user (Column 32, lines 25-28), wherein at least one of said graphics pipelines is configured to selectively super-sample (*post filtering, digitized analog video capture*, Column 5, lines 58-61;

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Column 33, lines 27-30) said graphical data rendered by said at least one graphics pipeline based on said input (Column 8, lines 7-9).

9. Regarding claim 4, representative of claims 6, 10, 14, 16 and 19, MacInnis discloses the system of claim 1, wherein: said first graphics pipeline is configured to super-sample said graphical data rendered by said first graphics pipeline; said second graphics pipeline is configured to super-sample said graphical data rendered by said second graphics pipeline; and said compositor is configured to average data values from said graphical data super-sampled by said first and second graphics pipeline to transmit said averaged data values to said display device (see above; Column 10, lines 41 through Column 11, line 39).

10. Regarding claim 5, representative of claim 22, MacInnis discloses the system of claim 1, wherein said compositor is configured to interface said graphical data received by said compositor with said display device via a scanning process (Figure 3, peripherals, Column 5, lines 17-18, Column 6, lines 67 through Column 7, line 2).

11. Regarding claim 7, representative of claims 8, 27, 28, 40, MacInnis discloses the system of claim 1, further comprising a third graphics pipeline configured to receive a plurality of graphics commands (Column 59, lines 20-23; Figure 69, Column 112, lines 24-33), said third graphics pipeline configured to transmit each of said graphics commands including three-dimensional graphical data (Column 59, line 9) to other graphics pipelines, said third graphics pipeline further configured to render two-dimensional graphical data (*2D, windows*



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*descriptors, x, y*, Column 15, lines 51-53; Column 16, lines 36-65 Column 59, line10) associated with the remaining graphics commands wherein said compositor is further configured to receive said two-dimensional graphical data rendered by said third graphics pipeline and to interface said two-dimensional graphical data with said display device, and wherein said first and second graphics pipelines are included in said other graphics pipelines (Figures 3 and 4) in a frame buffer (Column 32, lines 25-28, 46-54).

12. Regarding claim 9, representative of claims 11, 13, 17, 20, 24, 29, MacInnis discloses the system of claim 1, wherein: said first graphics pipeline is configured to receive an input identifying a first coordinate range (*windows descriptors, x, y, window controller*, Column 15, lines 51-53), said first graphics pipeline configured to discard (*blending, layers, at some point, there can be a single image representing the lower layers*, (Column 47, line 60-65), Column 45, line 55 through Column 51, line 33), based on said first coordinate range, a first portion of said graphical data transmitted from said graphics application, said first portion associated with coordinate values outside of said first coordinate range; and said second graphics pipeline is configured to receive an input identifying a second coordinate range, said second graphics pipeline configured to discard, based on said second coordinate range, a second portion of said graphical data transmitted from said graphics application, said second portion associated with coordinate values outside of said second coordinate range (*windows descriptors, x, y, window controller*, Column 15, lines 51-53).

MacInnis does not disclose a second portion of said image is based on said second graphical data portion, rendering said first and second graphical portions of the image. Jenkins

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teaches multiple pipelines (Column 60, lines 29-63); wherein said first and second images define at least a portion of a image; second rendering means for rendering a respective second portion of said graphical data, said second rendering means including a plurality of pipeline means for rendering said second graphical data portion in parallel and a compositing means for compositing said second rendered portion (Figures 1-4, Figure 13A, *sub-images, being handled by one of the sub-image processors*, Column 17, lines 52-55). The motivation for combining multiple pipeline processing with multiple sub-images with respective rendering means is for efficient use of available connection bandwidth and allow rapid synchronization with broadcast event stream, load balancing, computational efficiency, level of detail and resolution suited to human needs (Column 114, lines 51-54, 63-63; Column 117, lines 1-6, 24, 38-40, 48-50, 63-67, Column 118, lines 1-9, 17-28, 36-38, 55-65). Jenkins is evidence that at the time of the invention it would have been obvious to one skilled in the art of computer graphics processing to combine the benefits of parallel pipeline processing, as MacInnis discloses, with multiple sub-images rendering, as Jenkins teaches, for performance and display efficiencies.

13. Regarding claim 12, representative of claims 39, 41, 42, MacInnis discloses the system of claim 1, wherein said graphics application is configured to produce graphical data (Column 5, lines 25-28) that defines an object within said image (Column 50, lines 19-59), wherein said graphical data rendered by said first graphics pipeline defines a first portion of said object and wherein said graphical data rendered by said second graphics pipeline defines a second portion of said object (*windows descriptors, x, y, window controller*, Column 15, lines 51-53); [Claim 42] without rendering said first portion of said graphical object, (*blending, layers, at some point,*

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*there can be a single image representing the lower layers, (Column 47, line 60-65), Column 45, line 55 through Column 51, line 33).*

MacInnis does not disclose a second portion of said object in said image is based on said second graphical data portion, rendering said first and second graphical portions of the image. Jenkins teaches multiple pipelines (Column 60, lines 29-63); wherein said first and second images define at least a portion of a image; second rendering means for rendering a respective second portion of said graphical data, said second rendering means including a plurality of pipeline means for rendering said second graphical data portion in parallel and a compositing means for compositing said second rendered portion (Figures 1-4, Figure 13A, *sub-images, being handled by one of the sub-image processors*, Column 17, lines 52-55). The motivation for combining multiple pipeline processing with multiple sub-images with respective rendering means is for efficient use of available connection bandwidth and allow rapid synchronization with broadcast event stream, load balancing, computational efficiency, level of detail and resolution suited to human needs (Column 114, lines 51-54, 63-63; Column 117, lines 1-6, 24, 38-40, 48-50, 63-67, Column 118, lines 1-9, 17-28, 36-38, 55-65). Jenkins is evidence that at the time of the invention it would have been obvious to one skilled in the art of computer graphics processing to combine the benefits of parallel pipeline processing, as MacInnis discloses, with multiple sub-images rendering, as Jenkins teaches, for performance and display efficiencies.

14. Regarding claim 31, representative of claims 33, 35, 37, MacInnis discloses the system of claim 1, wherein each of said pipelines is implemented in hardware (Column 51, lines 26-29).

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15. Regarding claim 32, representative of claims 34, 36, 38, MacInnis discloses the system of claim 1, wherein each of said pipelines is implemented in software (Column 61, lines 11-14).

*Response to Arguments*

16. Applicant's arguments, see Pages 11-15, filed May 19, 2003, with respect to the rejection(s) of claim(s) 1-38 under 35 U.S.C. Sections 101, 102, 103 have been fully considered and are persuasive. Therefore, the rejection has been withdrawn.

17. However, upon further consideration, a new ground(s) of rejection is made in view of the judicially created doctrine of provisional obviousness-type double patenting. Applicant's amendments also necessitated new grounds of rejection.

18. Applicant asserts on page 16, line 4-6, "...there is nothing in MacInnis to indicate...from the same graphics application."

19. Examiner respectfully replies that MacInnis (US Patent 6,501,480, as cited in original office action mailed 2/13/2003) does disclose application and two pipelines, video and graphics (Column 3, lines 47-50; blending of them on one screen using logical windows, Column 4, lines 42-45, 51-54; Column 6, lines 30-45; Column 8, lines 61-63).

20. Applicant states that MacInnis does not disclose graphics pipelines render in parallel from the same graphical command (Page 18, 4 lines from bottom of the page) .

21. Examiner respectfully replies, however, that MacInnis discloses *in parallel* from the same graphical command (Column 45, line 11; Column 26, lines 14-16; 22-44).

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*Conclusion*

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Allen E. Quillen whose telephone number is (703) 605-4584. The examiner can normally be reached on Tuesday – Friday, 8:30am – noon and 1:00 - 4:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew C. Bella, can be reached on (703) 308-6829.

**Any response to this action should be mailed to:**

Commissioner of Patents and Trademarks

Washington, D.C. 20231

**Or FAX'd to:**


**(703) 872-9314 (for Technology Center 2600 only)**

Hand delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Sixth Floor (Receptionist), Arlington, Virginia

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number (703) 305-9600 or (703) 305-3800.

Allen E. Quillen  
Patent Examiner  
Art Unit 2676

\*\*\*August 2, 2003



**MATTHEW C. BELLA  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2600**